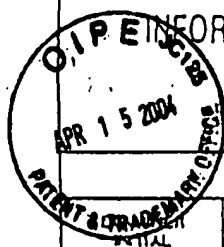


Form PTO-1449

Sheet 1 of 2



INFORMATION DISCLOSURE CITATION IN AN APPLICATION.

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10/763,304

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Shajan Mathew et al.

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U. S. PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	TITLE	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
T-D	6475874	11/5/02	Xiang et al.	438	396	12/7/00
	6503833	1/7/03	Ajmera et al.	438	682	11/15/00
	6534390	3/18/03	Chang et al.	438	592	1/16/02
	6436840	8/20/02	Besser et al.	438	721	10/19/00
	6440868	8/27/02	Besser et al.	438	721	10/19/00
	6326251	12/4/01	Gardner et al.	438	197	1/12/99
	6084279	7/4/00	Nguyen et al.	257	412	3/31/97
T-D	6475908	11/5/02	Lin et al.	438	659	10/18/01

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

T-D	"High Performance Damascene Metal Gate MOSFET's for 0.1um Regime", IEEE Trans. on Electron Devices, Vol. 47, No. 5, May 200, pp. 1028-1034 by Yagashita et al.
T-D	"CMOS Metal Replacement Gate Transistors using Tantalum Pentoxide Gate Insulator", by Chatterjee et al., IEDM 98-777, 29.1.1 to 29.1.4.

EXAMINER

T. DANG

DATE CONSIDERED

10/25/04

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